



Course Outcomes & CO-PO-PSO Mapping and Justification

| Subject | Computer Organization | 18CS34 |
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| COURSE OUTCOMES: | | |
| CO No. | On completion of this course, students will be able to: | Cognitive Level |
| 18CS34.1 | Understand basics of Computer Organization, concepts of program as sequences and operation of computers. | L2 |
| 18CS34.2 | Understand different ways of communication with I/O devices and standard I/O interfaces. | L2 |
| 18CS34.3 | Understand the basics of memory systems and cache Memories. | L2 |
| 18CS34.4 | Design arithmetic and logical operations with signed integer operands. | L3 |
| 18CS34.5 | Understand the basic processing unit and Pipelining | L2 |

CO-PO-PSO MAPPING

| CO No. | PO 1 | PO 2 | PO 3 | PO 4 | PO 5 | PO 6 | PO 7 | PO 8 | PO 9 | PO 10 | PO 11 | PO 12 | PSO 1 | PSO 2 | PSO 3 |
|---------------------|------------|------------|------------|------|------|------|------|------|------|-------|-------|------------|-------|-------|------------|
| 18CS34.1 | 2 | 1 | - | - | - | - | - | - | - | - | - | 1 | - | - | 2 |
| 18CS34.2 | 2 | 1 | 1 | - | - | - | - | - | - | - | - | 1 | - | - | 2 |
| 18CS34.3 | 2 | 1 | 1 | - | - | - | - | - | - | - | - | 1 | - | - | 2 |
| 18CS34.4 | 2 | 1 | 1 | - | - | - | - | - | - | - | - | 1 | - | - | 2 |
| 18CS34.5 | 2 | 1 | 1 | - | - | - | - | - | - | - | - | 1 | - | - | 2 |
| Avg. Mapping | 2.0 | 1.0 | 1.0 | - | - | - | - | - | - | - | - | 1.0 | - | - | 2.0 |

CO-PO-PSO JUSTIFICATION

| CO No. | PO/PSO | CL | Justification |
|---------------|---------------|-----------|---|
| 18CS34.1 | PO1 | 2 | Moderately mapped as students gain the knowledge on Computer Organization and the operation of computers. |
| | PO2 | 1 | Slightly mapped as students can identify functionalities and computing resources of computer systems |
| | PO12 | 1 | Slightly mapped as students apply the concepts of functionalities and computing resources of computer systems in continuing professional development. |
| | PSO3 | 2 | Moderately mapped as acquired knowledge helps students to provide novel approaches to the design of computer system. |
| 18CS34.2 | PO1 | 2 | Moderately mapped as students gain the knowledge on concepts of I/O communication and standard interface. |
| | PO2 | 1 | Slightly mapped as students can reframe the IO based system into interconnected subsystems. |
| | PO3 | 1 | Slightly mapped as students can use the knowledge gained to design different methods of communication with I/O devices |
| | PO12 | 1 | Slightly mapped as students apply the concepts of I/O communication and standard interface learnt in continuing professional development. |
| | PSO3 | 2 | Moderately mapped as acquired knowledge helps students to provide novel approaches to the design of IO interfaces. |
| 18CS34.3 | PO1 | 2 | Moderately mapped as students can understand the fundamentals of memory systems and cache Memories. |
| | PO2 | 1 | Slightly mapped as problem analysis is necessary to find solutions for in designing memory and cache memory. |
| | PO3 | 1 | Slightly mapped as students will be able to explore design alternatives in designing memory and cache memory systems. |
| | PO12 | 1 | Slightly mapped as students apply the concepts of designing memory and cache memory in continuing professional development. |
| | PSO3 | 2 | Moderately mapped as acquired knowledge helps students to provide novel approaches to the design of memory system. |
| 18CS34.4 | PO1 | 2 | Moderately mapped as students gain the knowledge on concepts of Signed integer arithmetic. |
| | PO2 | 1 | Slightly mapped as problem analysis is necessary to find solutions for the problems on integer arithmetic. |
| | PO3 | 1 | Slightly mapped as as students will be able to explore design alternatives in designing integer unit. |

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|----------|------|---|--|
| | PO12 | 1 | Slightly mapped as students apply the concepts of integer arithmetic learnt in continuing professional development. |
| | PSO3 | 2 | Moderately mapped as acquired knowledge helps students to provide novel approaches to the design of arithmetic unit. |
| 18CS34.5 | PO1 | 2 | Moderately mapped as students gain the knowledge on concepts of basics of processing unit and pipelining system. |
| | PO2 | 1 | Slightly mapped as problem analysis is necessary in designing basic processing unit. |
| | PO3 | 1 | Slightly mapped as students will be able to explore design alternatives in designing a basic processing unit. |
| | PO12 | 1 | Slightly mapped as students apply the concepts of basic processing unit in continuing professional development. |
| | PSO3 | 2 | Moderately mapped as acquired knowledge helps students to provide novel approaches to the design of basic processing unit. |

Prepared by

HoD

J Brundha Elci / Shilpa G V

Dr.M.Ramakrishna