



# VEMANA INSTITUTE OF TECHNOLOGY

Koramangala, Bengaluru - 34

Department of Computer Science & Engineering



<b>Subject</b>	<b>COMPUTER ORGANIZATION</b>	<b>17CS34</b>
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## COURSE OUTCOMES

CO No.	On completion of this course, students will be able to:	Cognitive Level
17CS34.1	Understand basics of Computer Organization, concepts of program as sequences and operation of computers.	<b>L2</b>
17CS34.2	Understand different ways of communication with I/O devices and standard I/O interfaces.	<b>L2</b>
17CS34.3	Understand the basics of memory systems and cache Memories.	<b>L2</b>
17CS34.4	Design arithmetic and logical operations with integer and floating-point operands.	<b>L6</b>
17CS34.5	Understand the basic processing unit, embedded and other large computing systems.	<b>L2</b>

## CO-PO-PSO MAPPING

CO No.	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
17CS34.1	2	1	-	-	-	-	-	-	-	-	-	1	-	-	2
17CS34.2	2	1	1	-	-	-	-	-	-	-	-	1	-	-	2
17CS34.3	2	1	1	-	-	-	-	-	-	-	-	1	-	-	2
17CS34.4	2	1	1	-	-	-	-	-	-	-	-	1	-	-	2
17CS34.5	2	1	1	-	-	-	-	-	-	-	-	1	-	-	2
17CS34	<b>2.0</b>	<b>1.0</b>	<b>1.0</b>	-	-	-	-	-	-	-	-	<b>1.0</b>	-	-	<b>2.0</b>

**CO-PO-PSO JUSTIFICATION**

<b>CO No.</b>	<b>PO/PSO</b>	<b>CL</b>	<b>Justification</b>
17CS34.1	PO1	2	Understand the fundamentals of Computer Organization and apply the same to explain the operation of computers.
	PO2	1	Identifies functionalities and computing resources of computer systems
	PO12	1	Identify the deficiencies and demonstrate the need of updating in the computer organization so that operation of computers.
	PSO3	2	Understand the computer organization and operation of computers.
17CS34.2	PO1	2	Understand the fundamentals of I/O communication and standard interface. Hence apply the same to solve problem related to communication.
	PO2	1	Able to interconnect the systems to communicate with outside world using I/O communication.
	PO3	1	Able to design different methods of communication with I/O devices as per the requirements.
	PO12	1	Identify the deficiencies and demonstrate the need of updating for the I/O communication to overcome the deficiency.
	PSO3	2	Understand the communication of system with the outside world using I/O devices.
17CS34.3	PO1	2	Understand the fundamentals of memory systems and cache Memories and apply the same in designing memory and cache memory.
	PO2	1	Able to identify the constraints in designing the memory and cache memory as per the performance requirement.
	PO3	1	Able to explore design alternatives in designing memory and cache memory systems.
	PO12	1	Able to analyze design alternatives of memory for continuing professional development.
	PSO3	2	Understand the memory system and design the memory system.
17CS34.4	PO1	2	Understand the fundamentals of arithmetic and logic unit and apply the same in designing the arithmetic unit and logical unit.
	PO2	1	Able to identify the algorithms and apply the same in designing arithmetic unit.
	PO3	1	Able to explore design alternatives in designing arithmetic and logical systems as per the requirement.
	PO12	1	Able to analyze design alternatives of arithmetic and logical unit for continuing professional development
	PSO3	2	Understand the arithmetic and logical operations.

17CS34.5	PO1	2	Understand the fundamentals of processing unit and embedded systems and apply the same to explain the working of embedded systems.
	PO2	1	Identifies functionalities and computing resources of embedded computer systems
	PO3	1	Able to implement and integrate the different modules of embedded systems.
	PO12	1	Able to analyze design alternatives of embedded systems for continuing professional development
	PSO3	2	Understand the basics of processing unit and working of embedded systems.

**Prepared by:**

**(Shilpa G V & Shilpa Reddy )**

**Approved by:**

**(H.o.D)**