

Course: Advanced Computer Architecture

Scheme:2010

Course Code: 10CS74

Sem: 7

	Course Outcome	POs/ PSOs	CL	Class Sessions (approximate)	Tutorial (Hrs)	Lab Sessions (Hrs)
CO 1	Understand the cost, performance, Trends in Technology, power in Integrated Circuits and Principles of computer design.	PO1, PSO3	U	8	NA	NA
CO 2	Analyze the working of pipelining, exploring instruction level parallelism using static, dynamic & advanced techniques of scheduling.	PO1 PO3, PSO3	A	9	NA	NA
CO 3	Analyze the techniques to explore Instruction level parallelism and reducing the cost & hazards using dynamic scheduling.	PO1 PSO3	A	9	NA	NA
CO 4	Analyze multiprocessors & thread level parallelism using shared, distributed and directory based memory models.	PO1 PO2. PSO3	A	8	NA	NA
CO 5	Analyze cache performance, cache optimizations, memory technologies, Protection via virtual memory and virtual machines.	PO1 PO2 PSO3	A	10	NA	NA
CO 6	Understand hardware & software for VLIW & EPIC for parallelism, scheduling & speculation.	PO1 PSO3	U	8	NA	NA
Total Hours of instruction				52		