Course: Electronic Circuits Scheme:2010

Course Code: (10CS32) Sem:3

	Course Outcome	POs/ PSOs	CL	Class Sessions (approximate)	Tutorial (Hrs)	Lab Sessions (Hrs)
CO1	Determine the operating point for Fixed bias, self-bias, voltage divider bias and collector to base bias circuits	PSO3 ,PO2	Ap	11	NA	NA
CO2	Describe the working of diodes, FET's and BJT's	PSO3 ,PO1	U	10	NA	NA
CO3	Describe the characteristics of Photo emitters, photo sensors , optoelectronic devices and working of amplifiers.	PSO3 ,PO1	R	10	NA	NA
CO4	Understand the working RC, LC, Crystal oscillators and wave shaping circuits.	PSO3 ,PO1	U	11	NA	NA
CO5	Describe the working of Linear power supplies ,Switched mode power supplies and op-amp.	PSO3 ,PO1	R	10	NA	NA
	Total Hours of instruction			52		

Course: **EC & LD LAB** **Scheme: 2010**

Course Code:(10CSL38) **Sem:3**

	Course Outcome	POs/ PSOs	CL	Class Sessions (approximate)	Tutorial (Hrs)	Lab Sessions (Hrs)
CO1	Understand the working of diode, resistance, capacitance, enhanced MOSFET components,555 Timer.	PSO3 ,PO1 ,PO2	U	NA	NA	9

CO2	Implement an op-amp as Schmitt trigger, relaxation oscillator, R-2R ladder.	PSO3 ,PO2	Ap	NA	NA	8
CO3	Analyze frequency response, phase operations, voltage gain of C E amplifier	PSO3 ,PO2	A	NA	NA	5
CO4	Design simple and combinational sequential circuits.	PSO3 ,PO3	C	NA	NA	12
CO5	Understand the working of simple Verilog modules using Xilinx software and electronic circuits p-spice tool software.	PSO3 ,PO3	U	NA	NA	8
Total Hours of instruction						42