

**Academic Year: 2015-16/2016-17(ODD)**

**Scheme: 2015**

**Course: Analog and Digital Electronics (15CS32)**

**SEM: 3**

**Instructors: Rashmi R/ Kavitha Bai**

	Course Outcome	POs/ PSOs	CL	Class Sessions (approximate)	Tutorial (Hrs)	Lab Sessions (Hrs)
CO1	Understand the concepts of JFETs, MOSFETs, BJT and op-amps.	PSO3, PO1	U	10	NA	NA
CO2	Analyze Combinational Logic circuits, Simplification of Algebraic Equations using Karnaugh Maps and Quine McClusky Techniques.	PSO3,PO 2,PO3	Ap	11	NA	NA
CO3	Design Digital multiplexers, Adders and Subtractors, Binary Comparators, Latches and Master-Slave Flip-Flops.	PSO3,PO 2,PO3	Ap	11	NA	NA
CO4	Analyze Synchronous and Asynchronous Sequential circuits	PO2, PO4, PSO3	A	9	NA	NA
CO5	Explain registers and Counters, A/D and D/A converters.	PO2, PSO3	U	11	NA	NA
Total Hours of instruction				50		