

Academic Year: 2015-16 and 2016-17 (ODD)

Scheme : 2015

Course: Analog and Digital electronics lab (15CSL37)

Sem : 3

Instructors: Rashmi R/ Kavitha Bai

	Course Outcome	POs/ PSOs	CL	Class Sessions (approximate)	Tutorial (Hrs)	Lab Sessions (Hrs)
CO 1	Design different types of combinational logic circuits(adders ,EVM, Code converters) using gates.	PO3, PSO3	C	NA	NA	10
CO 2	Design different types of counters and Registers using Flip-flops.	PO3, PSO3	C	NA	NA	12
CO 3	Use simulation package to design counters, Flipflops and multiplexers.	PO3, PSO3	AP	NA	NA	10
CO 4	Construct op-amps ,multi vibrator using H/W and S/W.	PO3, PSO3	C	NA	NA	8
Total Hours of instruction						40