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College	Vemana Institute of Technology, Kormangala, Bengaluru 560034							
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Department	Computer Science (Cyber Security)							
Date of Joining the Institution	08/03/2006							
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Qualification	Under Graduate		Post Graduate				Doctoral Degree	
	BE		M Tech				Ph.D	
Area of specialization	Electronics and Communication		Digital Electronics and Communication				Electronics and Communication	
Title of Doctoral degree	Evaluation and Efficient Implementation of Error Coding Techniques for OFDM Physical Layer Architecture in Underwater Communication System							
Total Experience in Years	Teaching	23	Research	5	Industry	0	Other	0
Papers Published in Journals	National	-		International		05		
Papers Presented in Conferences	National	-		International		01		
Projects Carried out	UG: 10							
Ph.D Students guiding	02							
Responsibilities	NBA coordinator, Internship Coordinator, IIIC coordinator							
Patents filed & granted	1. A METHOD FOR ERROR DETECTION AND ERROR CORRECTION FOR UNDERWATER COMMUNICATION BASED ON ADAPTIVE TURBO DECODER WITH FEEDFORWARD MESSAGE DECODING AND MEMORY LOGIC FOR FAST DECODING Application No.202041023836 A Date of filing of Application :06/06/2020 Publication Date : 12/02/2021 2. A METHOD FOR PERFORMING OFDM MODULATION AND DEMODULATION BASED ON DTCWT WITH N SUB CARRIERS DESIGNED USING PIPELINED-FOLDED-REUSABLE SYSTOLIC ARRAY ALGORITHM WITH RECONFIGURABLE LOGIC							

	Application No.202041023845 A Date of filing of Application :06/06/2020 Publication Date : 12/02/2021 https://search.ipindia.gov.in/DynamicUtility/Journal/Patent 3. AUTOMATED AERIAL VEHICLE Design no. - 354840-001 4. WICK-BASED HYDROPONICS SYSTEM WITH MOBILE APPLICATION Application No. 202541007374
Journals Published	1. Evaluation of WATERMARK Channel for Underwater Communication using Dual Tree Complex Wavelet Transform based Orthogonal Frequency Division Multiplexing Model Nanjareddy, G. , Boregowda, V.M. , Maroof, N. International Journal of Computer Network and Information Security, Volume 15, Year 2023, Pages 16-29 2. Low Power Area Optimum Configurable 160 to 2560 Subcarrier Orthogonal Frequency Division Multiplexing Modulator-Demodulator Architecture based on Systolic Array and Distributive Arithmetic Look-Up Table Nanjareddy G.;Boregowda V.M.;Raj C.P. Informacije MIDEM - Journal of Microelectronics, Electronic Components and Materials, Volume 51, Year 2021, Pages 119-134 3. Low power design of adaptive turbo decoder with feedforward message decoding and look-up-table approach Journal of Theoretical and Applied Information Technology, Year 2020 4. 180 Mbps Viterbi Decoder Design on FPGA for OFDM Modulator in Underwater Communication Applications Lecture Notes on Data Engineering and Communications Technologies, Year 2019 5. High speed low power adaptive viterbi decoder architecture for underwater acoustic communication with turbo codes International Journal of Applied Engineering Research, Year 2017
Grants received	01 (KSCST)
Professional Memberships	MISTE
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