

Course: LOGIC DESIGN

Scheme:2010

Course Code:(10CS32)

Sem:3

	Course Outcome	POs/ PSOs	CL	Class Sessions (approximate)	Tutorial (Hrs)	Lab Sessions (Hrs)
CO1	Understand binary number system, logic gates, and Boolean laws.	PSO3 ,PO1	U	9	NA	NA
CO2	Analyze the Boolean equations for SOP and POS expressions using k-map , Entered variable Mapping and Q-M method.	PSO3 ,PO1 ,po2	A	9	NA	NA
CO3	Analyze characteristics equation for sequential circuits using flip flop	PSO3 ,PO2 ,PO3	A	8	NA	NA
CO4	Implement different encoders using Multiplexers	PSO3 ,PO2 ,PO3	Ap	8	NA	NA
CO5	Understand the working of sequential circuits with shift registers and analog to digital and digital to analog convertors.	PSO3 ,PO2	Ap	9	NA	NA
CO6	Analyze synchronous and asynchronous counters for sequential circuits	PSO3 PO2, PO4	A	9	NA	NA
Total Hours of instruction				52		21